

GA-F2A85XM-HD3

PAGE TITLE *Revision : 1.2*

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	APU DDRIII MEMORY
05	APU CONTROL
06	APU UMI, GFX, GPP
07	APU POWER & GND
08	DDRIII CHANNEL A
09	HUDSON UMI, PCIE, PCI, CPU, LPC
10	HUDSON ACPI, GPIO, USB, AUDIO
11	HUDSON SATA,SPI,HWM
12	HUDSON POWER & GND
13	PCIE x16 , x1
14	PCI SLOT ,HDMI
15	ITE 8728 , Dual-BIOS , KB/MS
16	COM ,RGB ,DVI
17	FAN , HWMO , F_USB
18	ALC887-VD2 CODEC
19	ATX, FRONT PANEL
20	POWER SEQUENCE
21	VCORE (ISL62773)
22	VCORE MOS
23	DDR POWER, 5VDUAL , ERP
24	RTL8111F-VL
25	VCC_SB , APU_VDDP , VCC11_DUAL , VDDA25

APU_VDDIO_SUS=DDR15V

APU_VTT_SUS=DDRVTT

APU_VDDP_RUN=APU_VDDR_RUN=APU_VDDP

+1.1V_RUN=FCH_VDD_11_RUN=VCC_SB

+3.3V_RUN=VCC3

+3.3V_ALW=3VDUAL

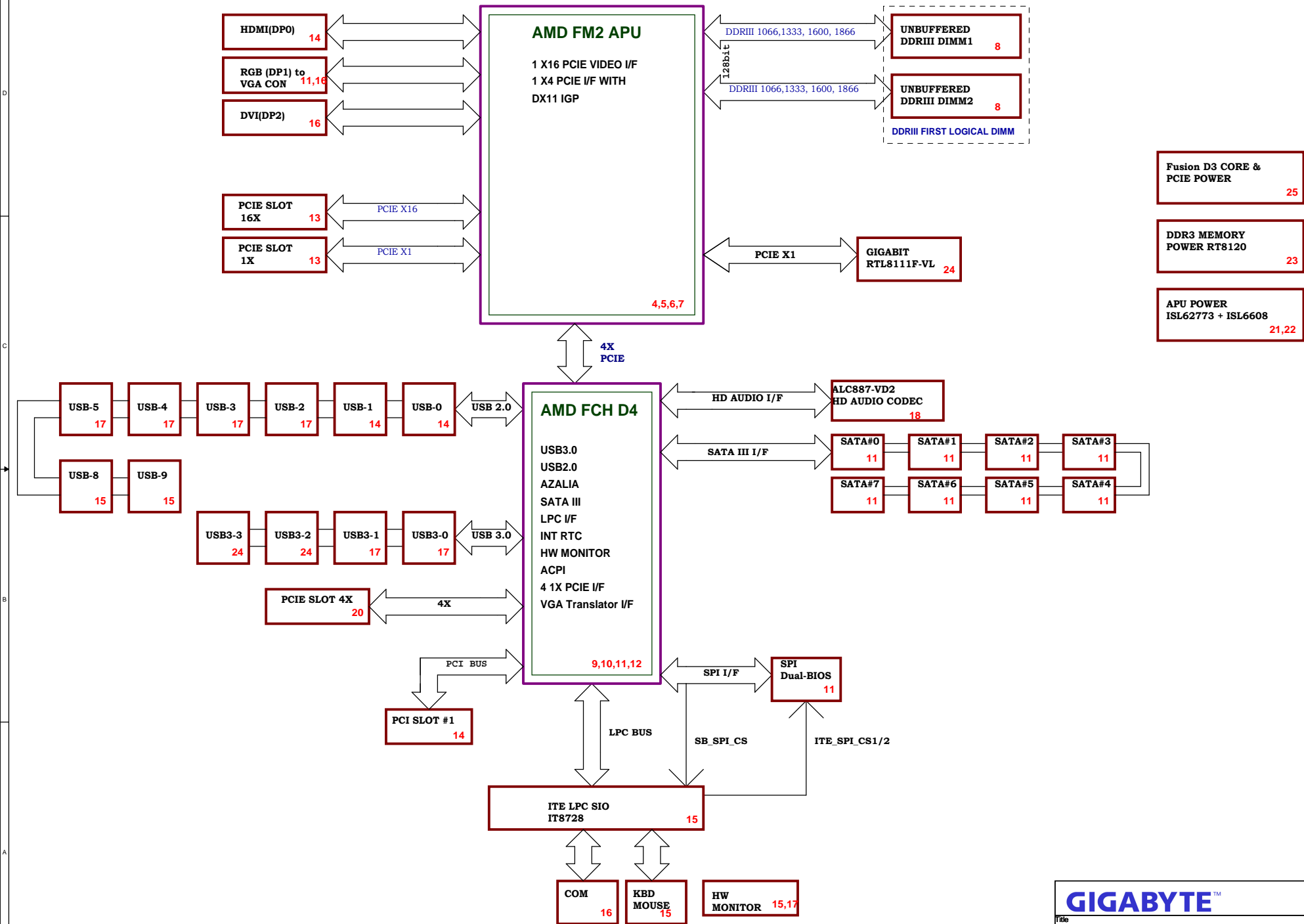
GIGABYTE™			
Title COVER SHEET			
Size	Document Number	Rev	
Custom	GA-F2A85XM-HD3	1.2	
Date:	Thursday, April 11, 2013	Sheet	1 of 25

Circuit or PCB layout change for next version

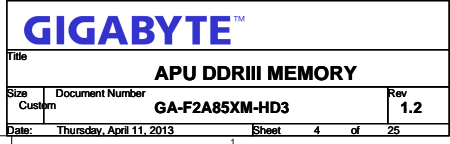
Component value change history

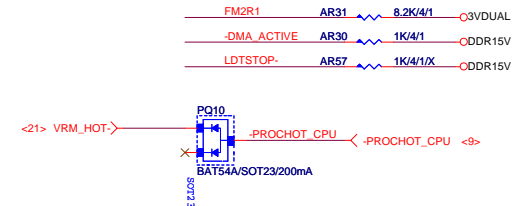
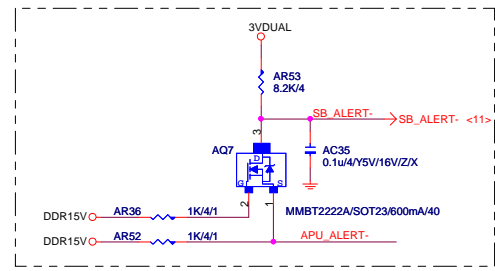
P-Code: U12065-0
243.84*174/B/50+/-15%/BLUE

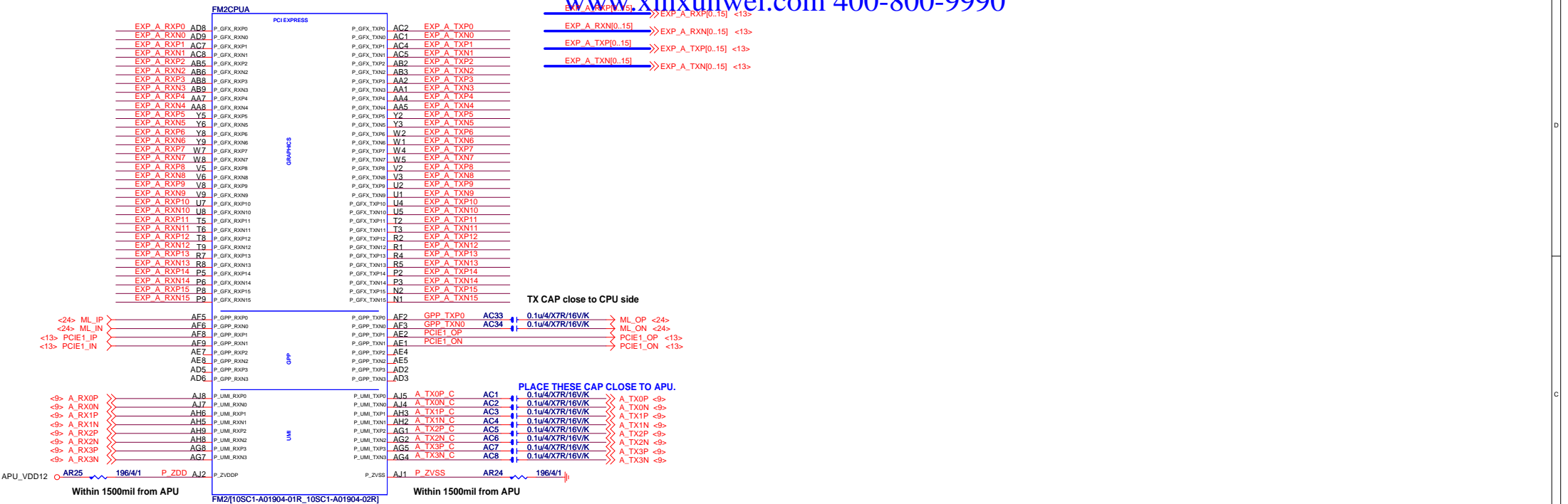
[illegible][illegible]

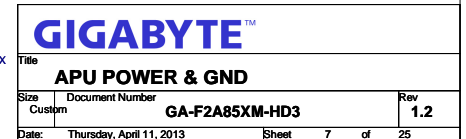
**GIGABYTE™****BLOCK DIAGRAM**

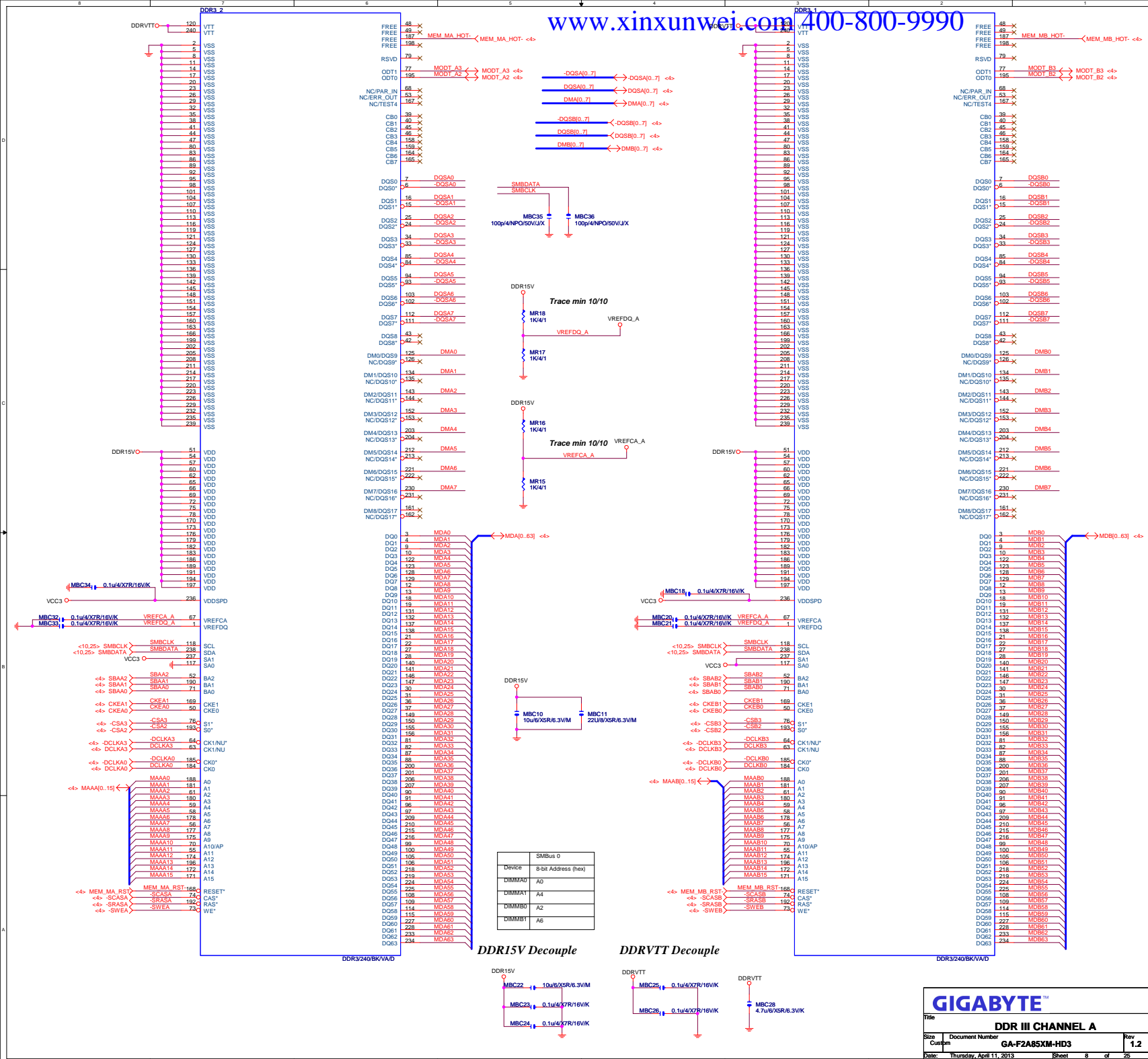
Title	Document Number	Rev
Size	GA-F2A85XM-HD3	1.2
Custom		
Date:	Thursday, April 11, 2013	Sheet 3 of 25

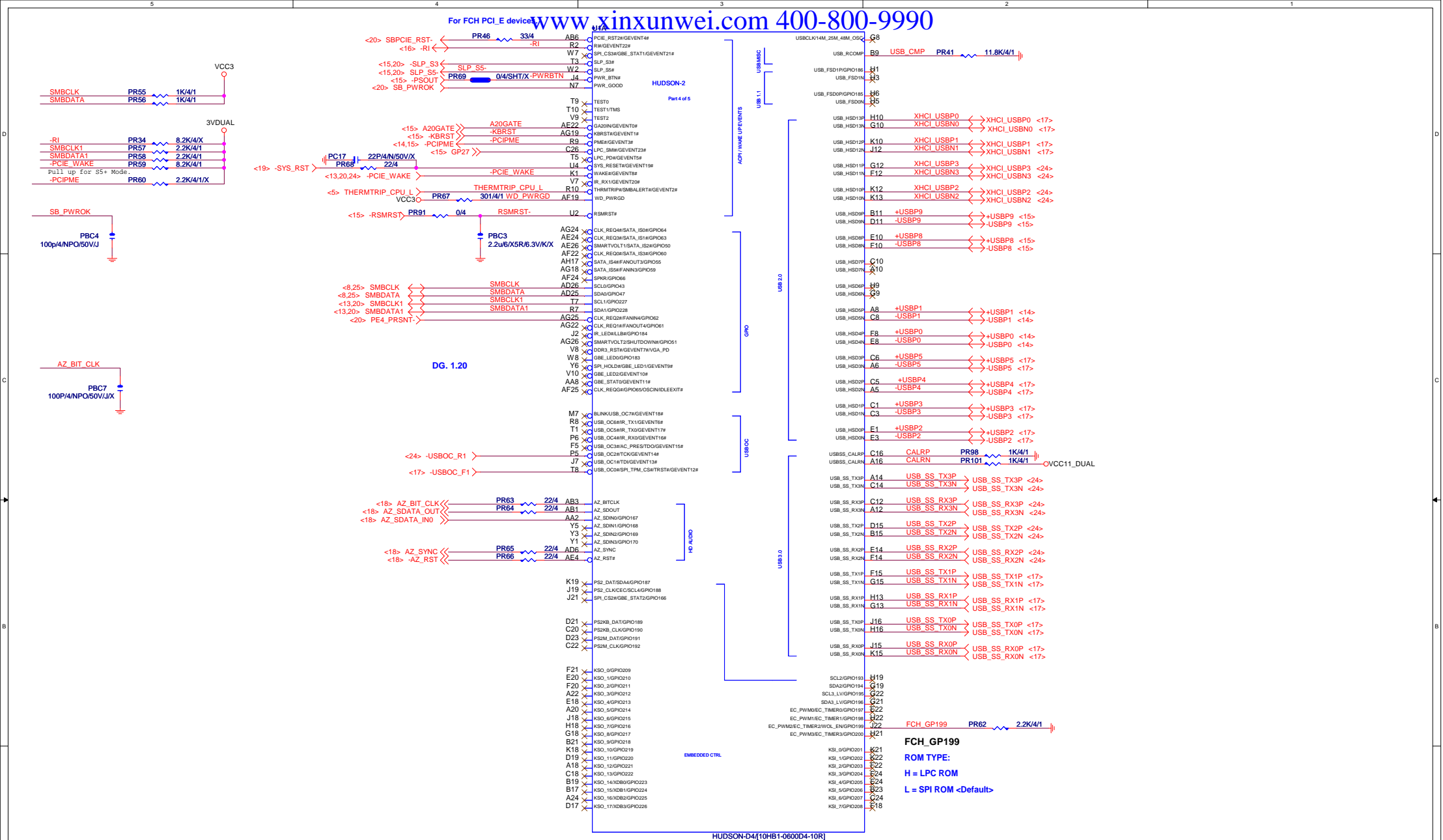


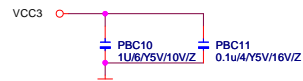






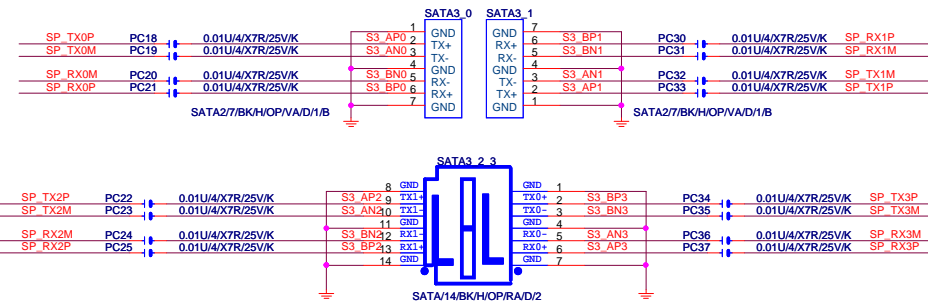
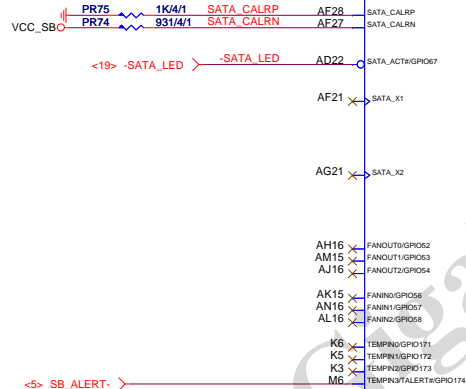






SATA 6~7 for Hudson D4.

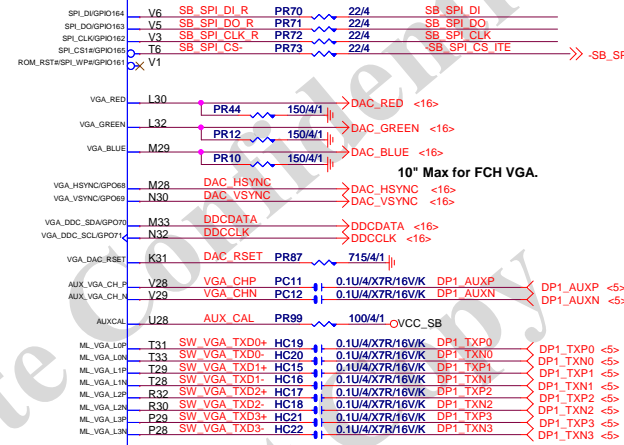
PLACE SATA_CAL RES VERY CLOSE TO BALL OF U1



Part 2 of 5

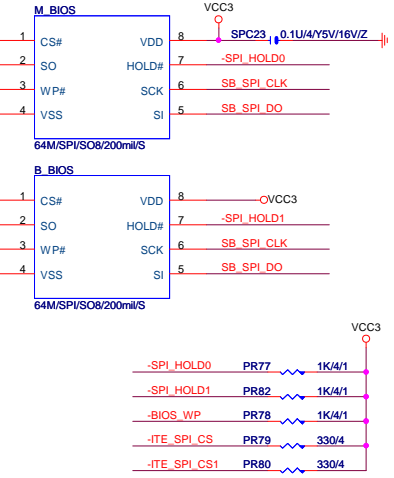
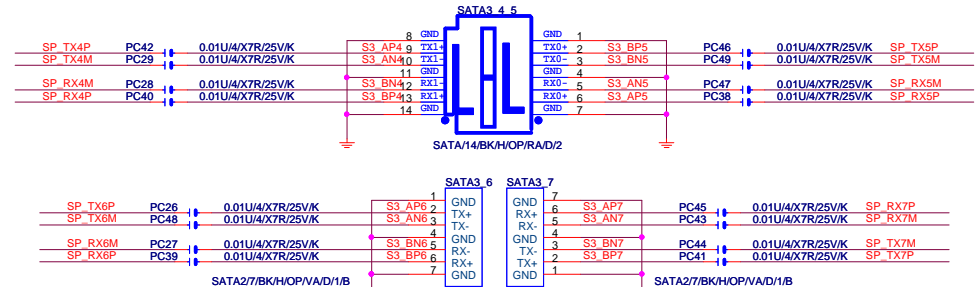
SD_CLK#GCLK_2GPI073 AL14
SD_CMD#GCLK_2GPI074 AN14
SD_CD#GPI075 AJ12
SD_WP#GPI076 AH12
SD_DATA0SDAT1_2GPI077 AK13
SD_DATA1SDAT2_2GPI078 AM13
SD_DATA2GPI079 AH15
SD_DATA3GPI080 AJ14

GBE_COL AC4
GBE_CRS AD3
GBE_MDC# AD9
GBE_MDIO W10
GBE_RXCLK# AB8
GBE_RXD0 AH7
GBE_RXD2 AF7
GBE_RXD1 AE7
GBE_RXD0 AD7
GBE_RXCTL#RXDV AG8
GBE_RXEN# AD1
GBE_TXCLK# AB7
GBE_TXD3 AF9
GBE_TXD2 AG6
GBE_TXD1 AE9
GBE_TXD0 AD8
GBE_TXCTL#TXEN AB9
GBE_PHY_PD AC2
GBE_PHY_RST# AA7
GBE_PHY_INT# W3

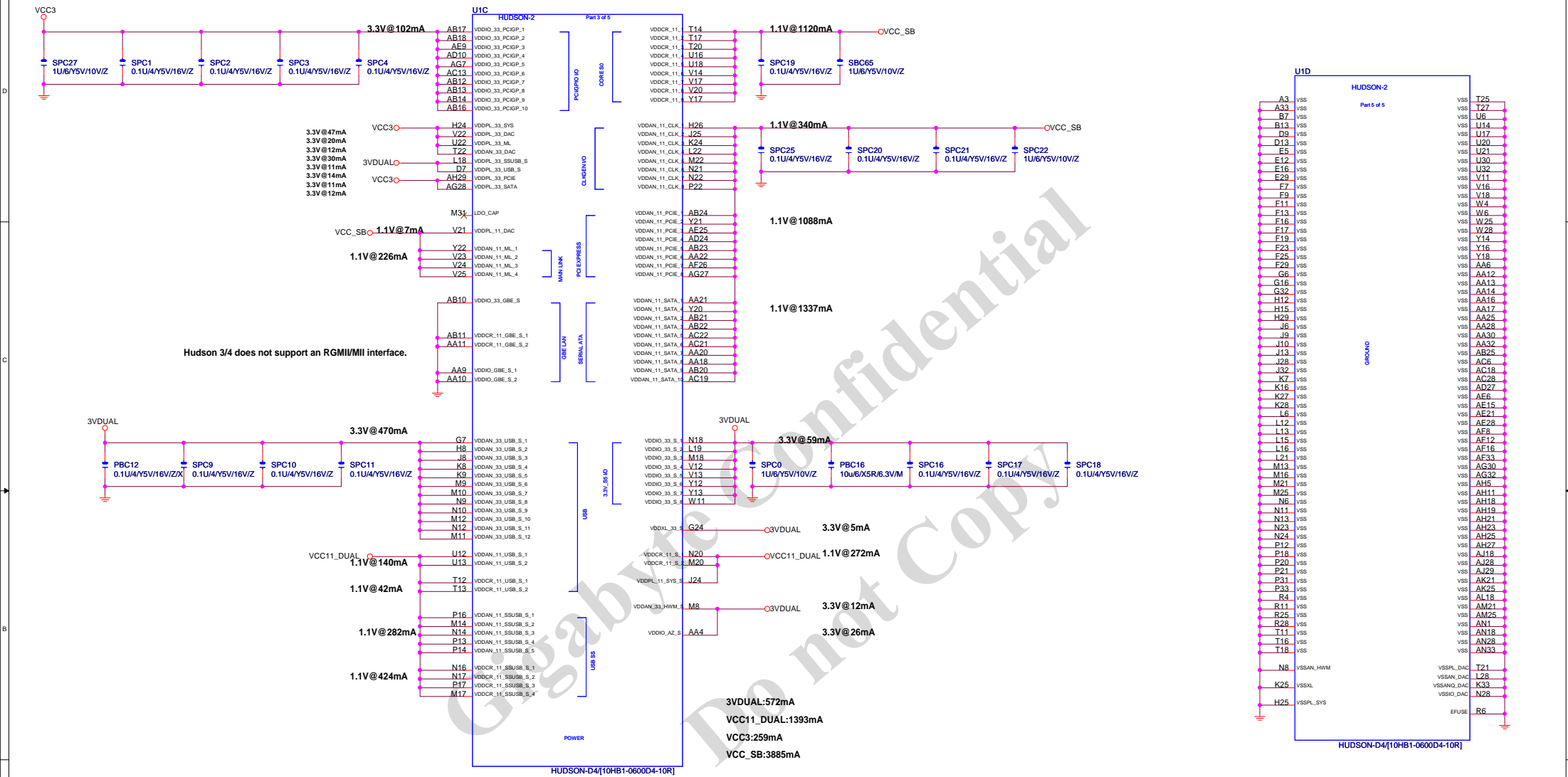


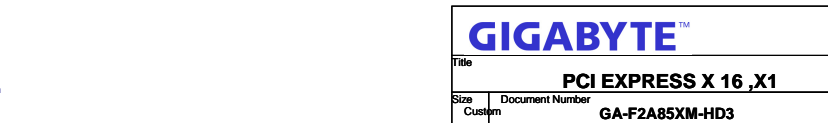
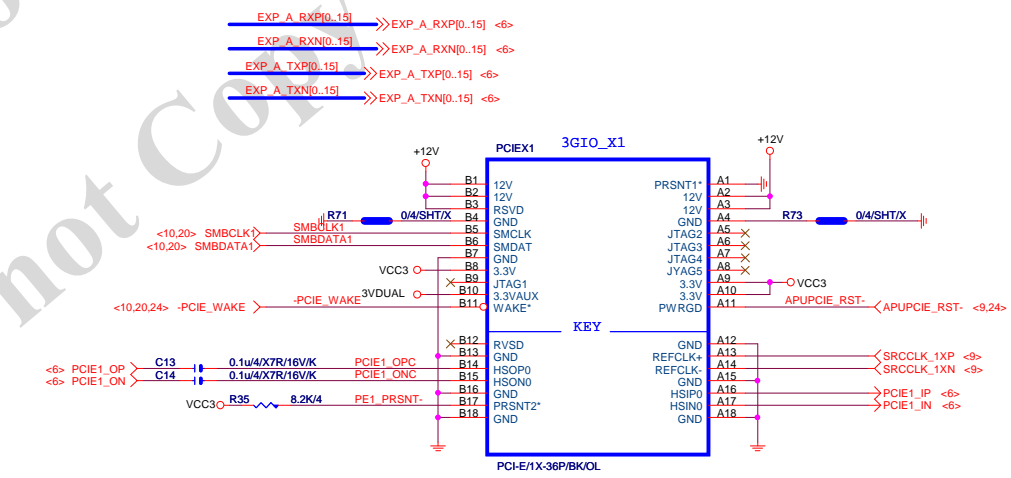
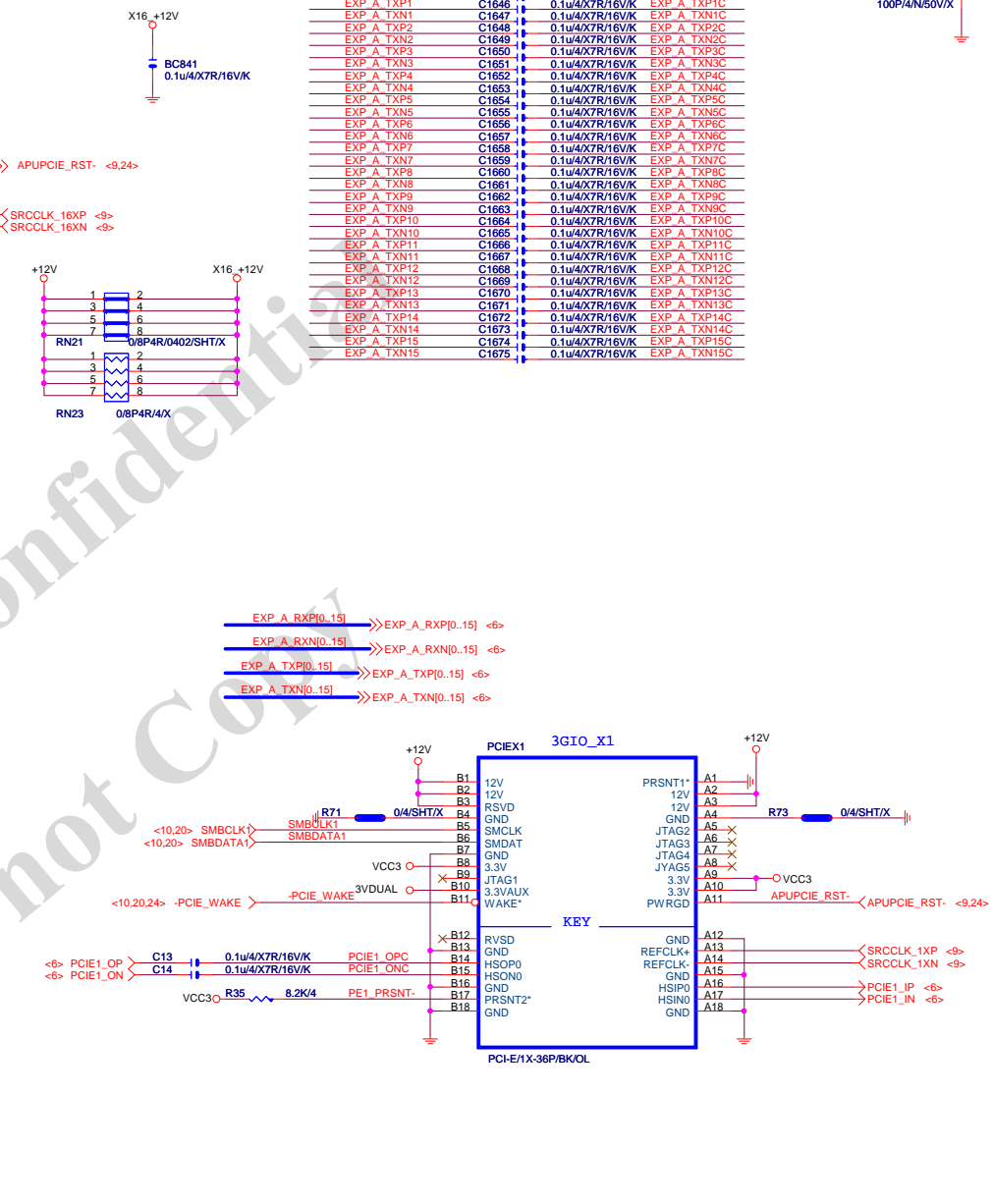
VIN0GPI0175 N2
VIN1GPI0176 M3
VIN2SDAT1_1GPI0177 L2
VIN3SDAT2_1GPI0178 N4
VIN4SLOAD_1GPI0179 P1
VIN5CLK_1GPI0180 P3
VIN6GBE_STAT3GPI0181 M1
VIN7GBE_LED3GPI0182 M5

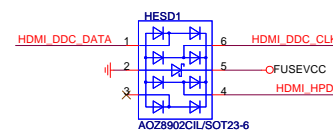
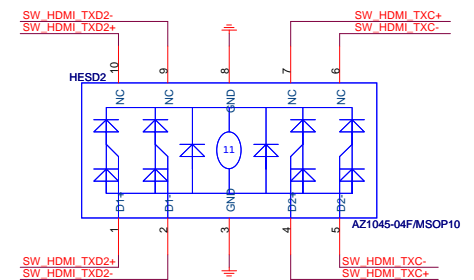
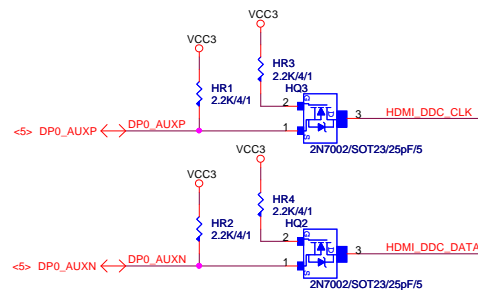
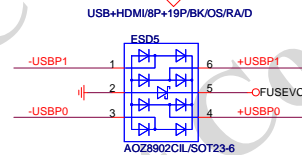
HUDSON-D4[10HB1-0600D4-10R]

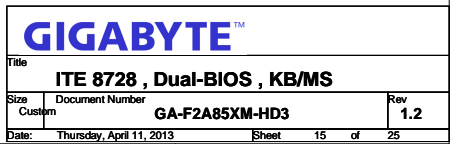


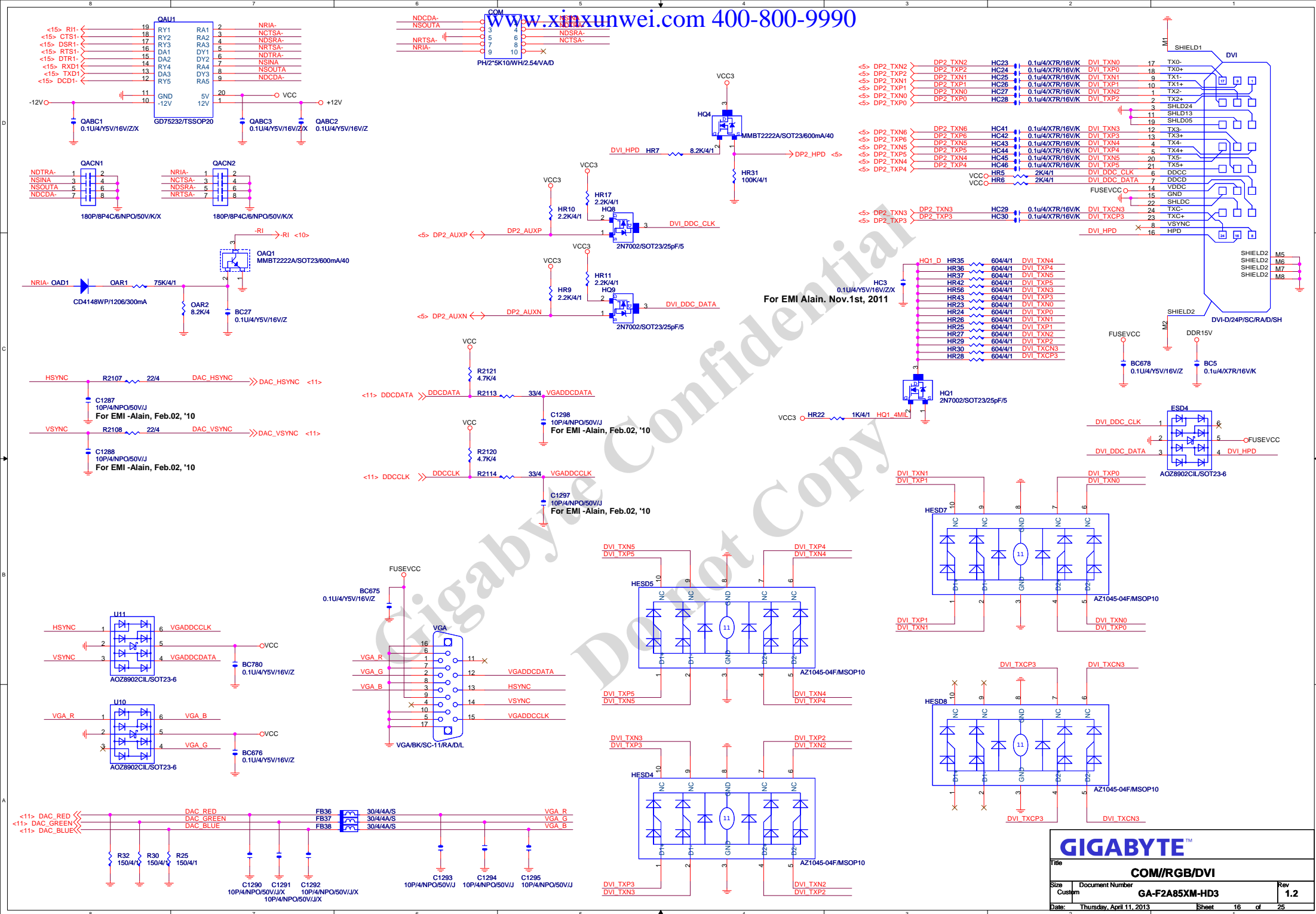
Fix flash BIOS fail issue 1K to 330 ohm

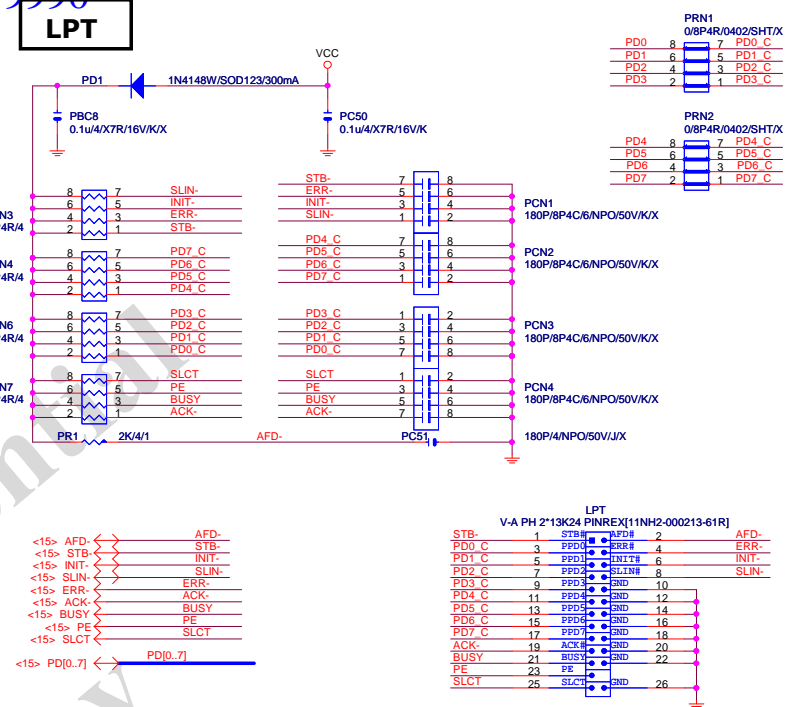




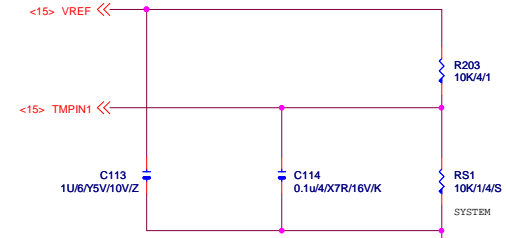




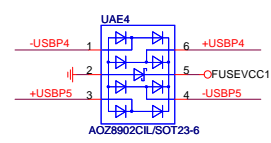




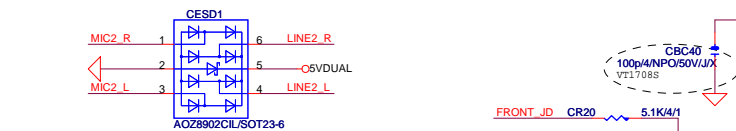
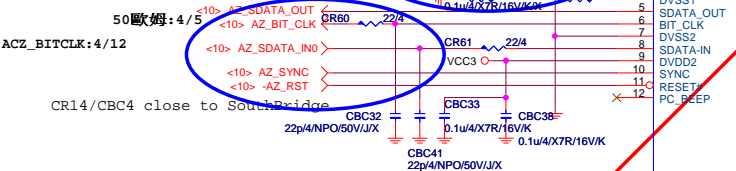
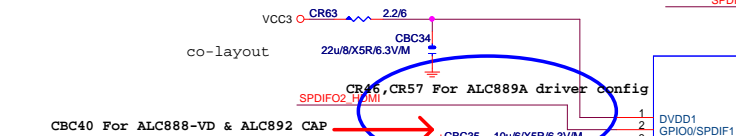
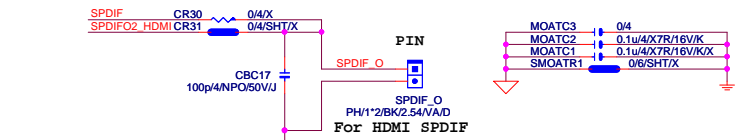
Hardware Monitor circuits



FRONT SIDE USB2



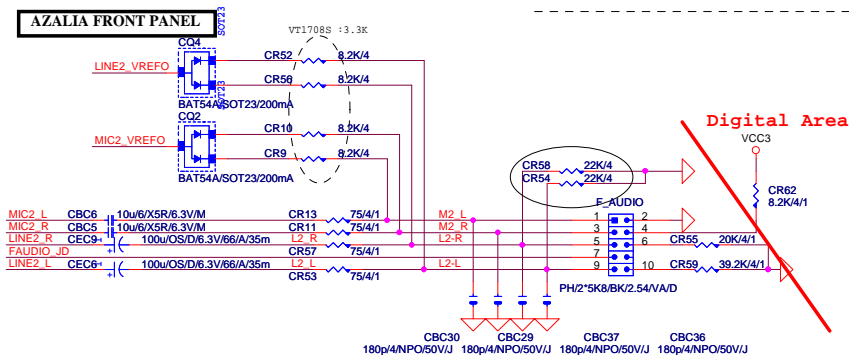
SPDIF_OUT



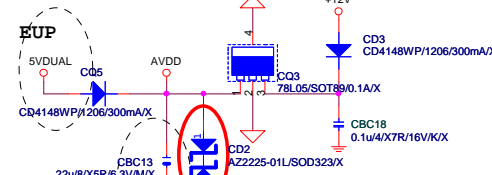
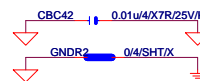
AZALIA CODEC ALC887-VD2/ALC889/VT1708S/VT1708SCE Colay

	ALC887-VD2	ALC889	VT1708S	VT1708SCE
CR65	X	O	O	X
CR64	X	X	X	O
CR44/CBC26	47ohm+1nF	47ohm+1nF	22ohm+100P	22ohm+100P
CR34	20K/1%	20K/1%	5.1K/1%	20K/1%
CR31	O	O	O	O
CR30	X	X	X	X
CBC1/CBC2	22uF/X5R	22uF/X5R	22uF/X5R	22uF/X5R
CR20	5.11K/4/1	5.11K/4/1	5.1K/4/1	5.1K/4/1
CBC35	O	X	X	O
CBC39/CBC40	N/A	N/A	100P/4	100P/4
CR6/CR7/CR54/CR58	22K/4	22K/4	10K/4	10K/4
CR5/CR8/CR13/CR11/CR57/CR53	75 ohm	62 ohm	75 ohm	75 ohm
CR51/CD1/CBC7	O	X	X	O
CD2/CD3/CQ3/CQ5	X	O	O	X
CR1/CR14/CR17/CR22	75 ohm	62 ohm	1K ohm	1K ohm

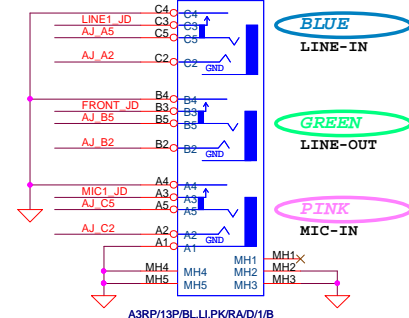
AZALIA FRONT PANEL



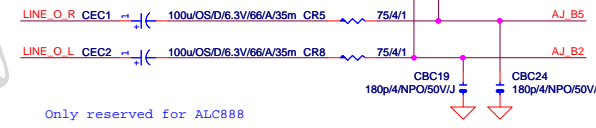
CODEC POWER/EMI PAD



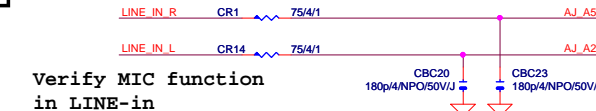
上ALC892時,此類電容要保留
ADD CD2 For ESD PROTECT DIODE



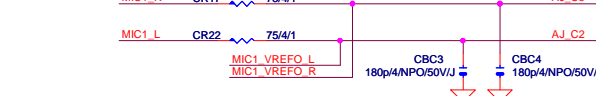
LINE-OUT



LINE-IN

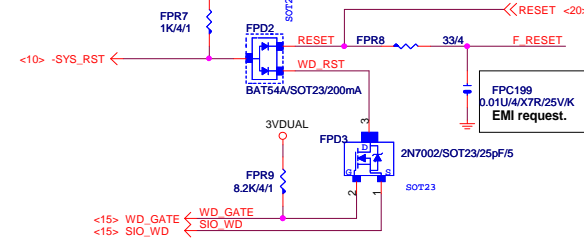
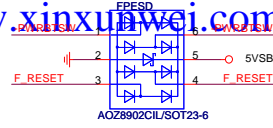
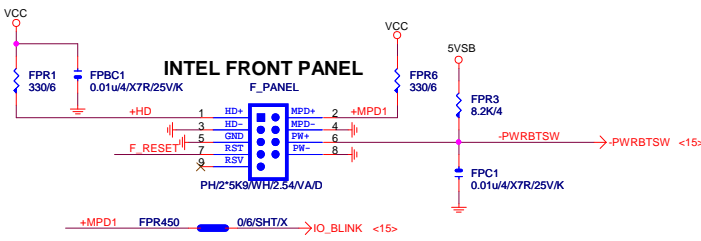


MIC-IN

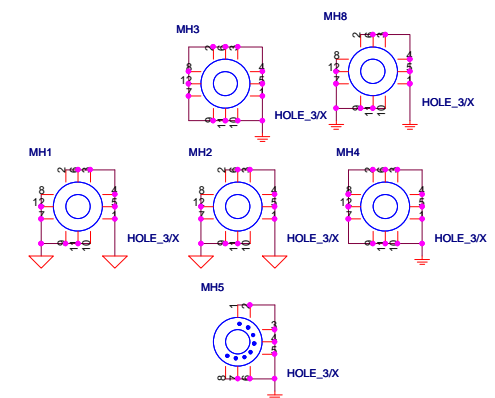
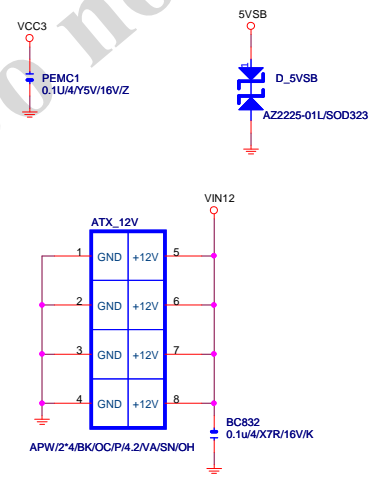
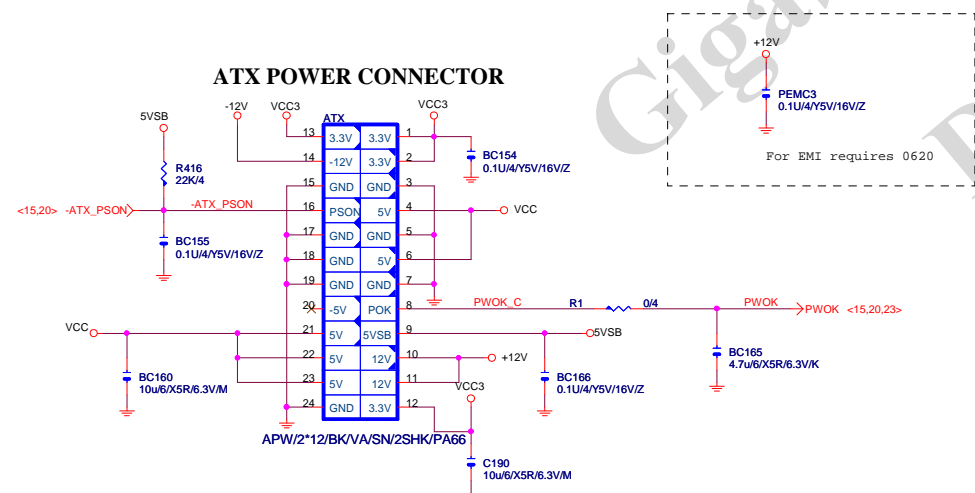
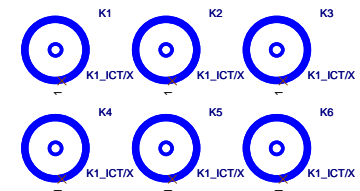
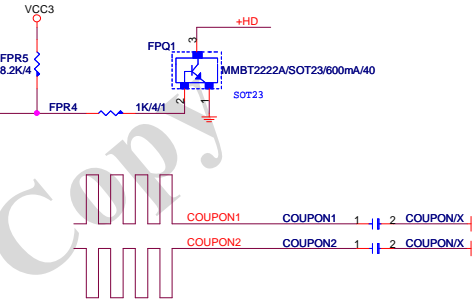
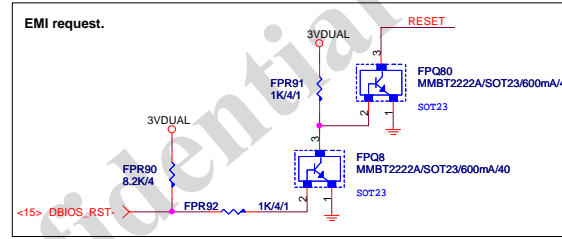
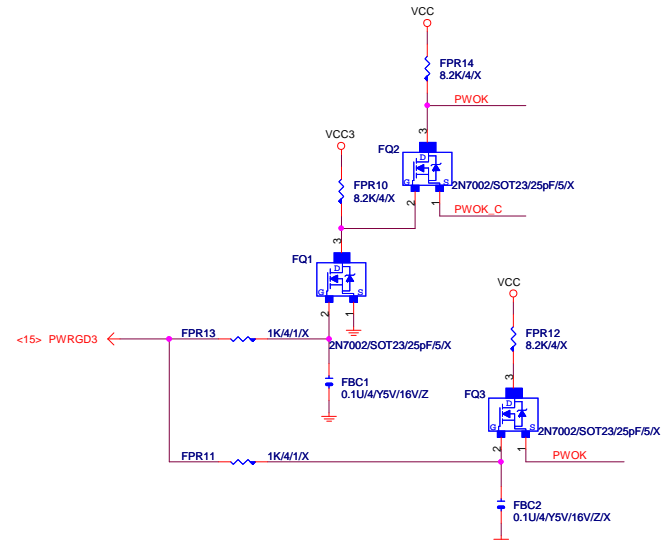


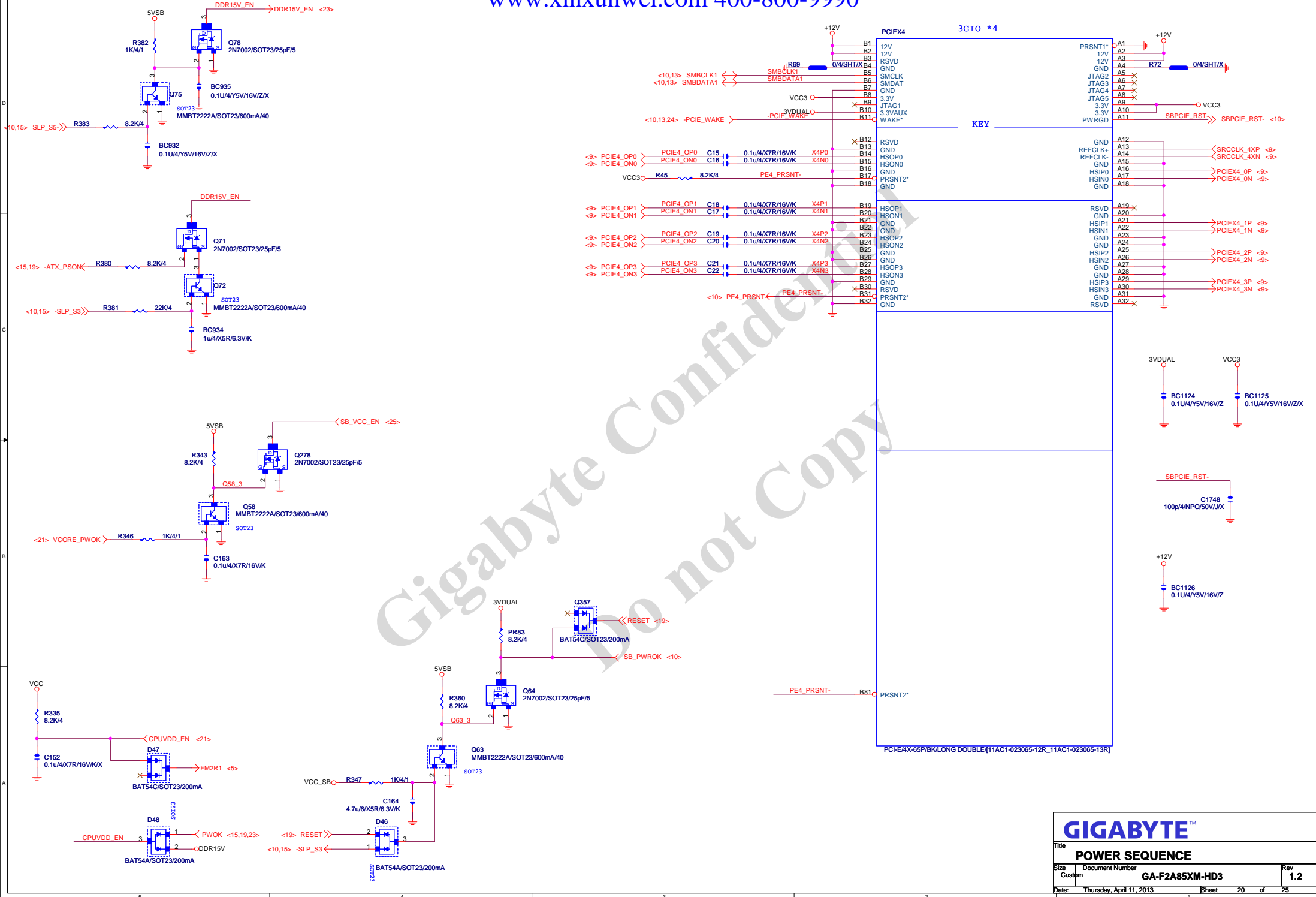
GIGABYTE

Title	HD AUDIO ALC887-VD2	Rev	1.2
Size	Document Number	GA-F2A85XM-HD3	
Date	Thursday, April 11, 2013	Sheet	18 of GIGABYTE

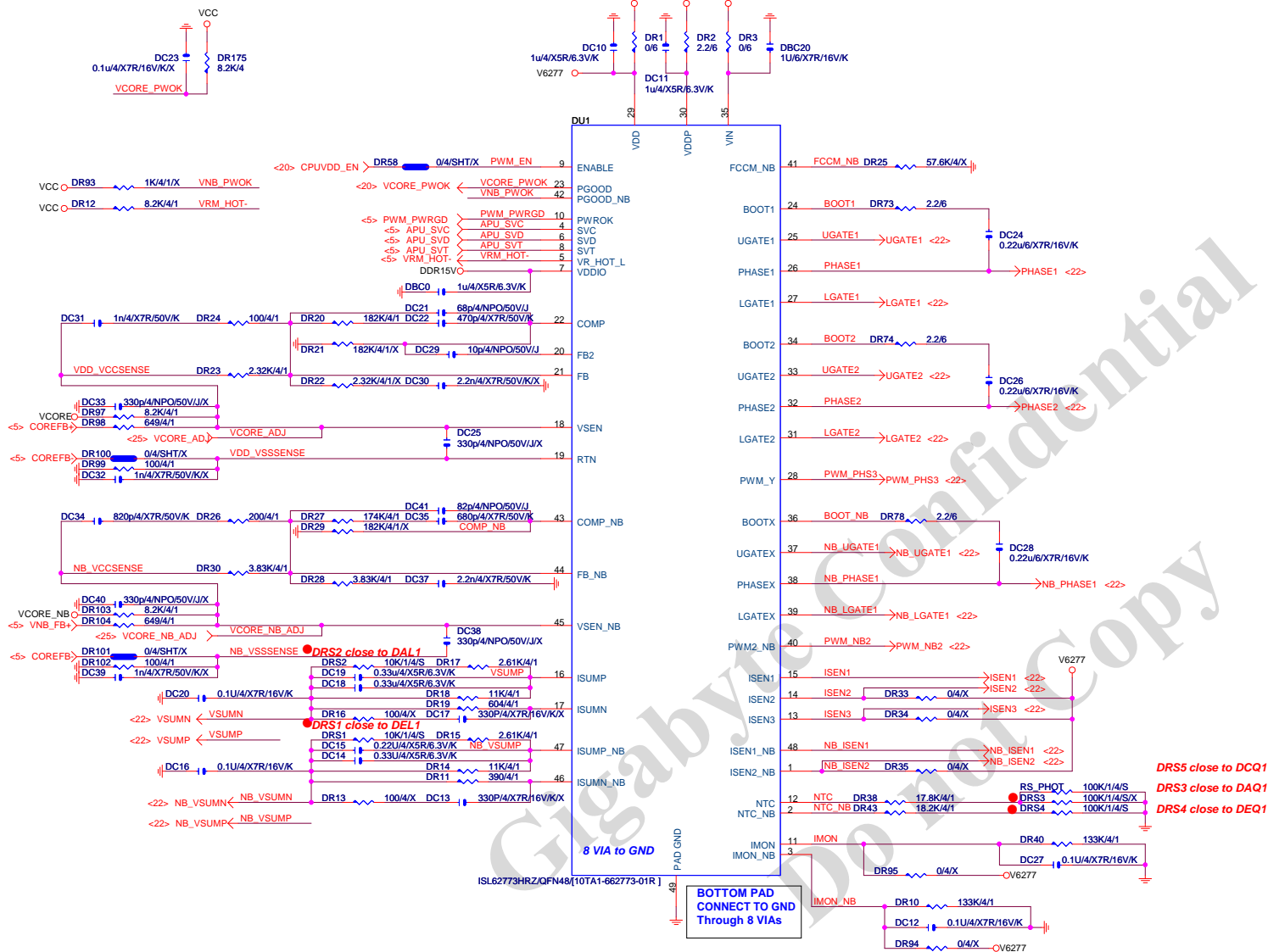


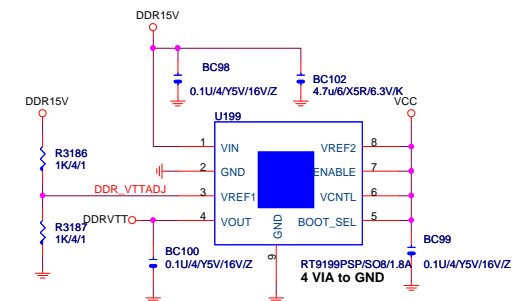
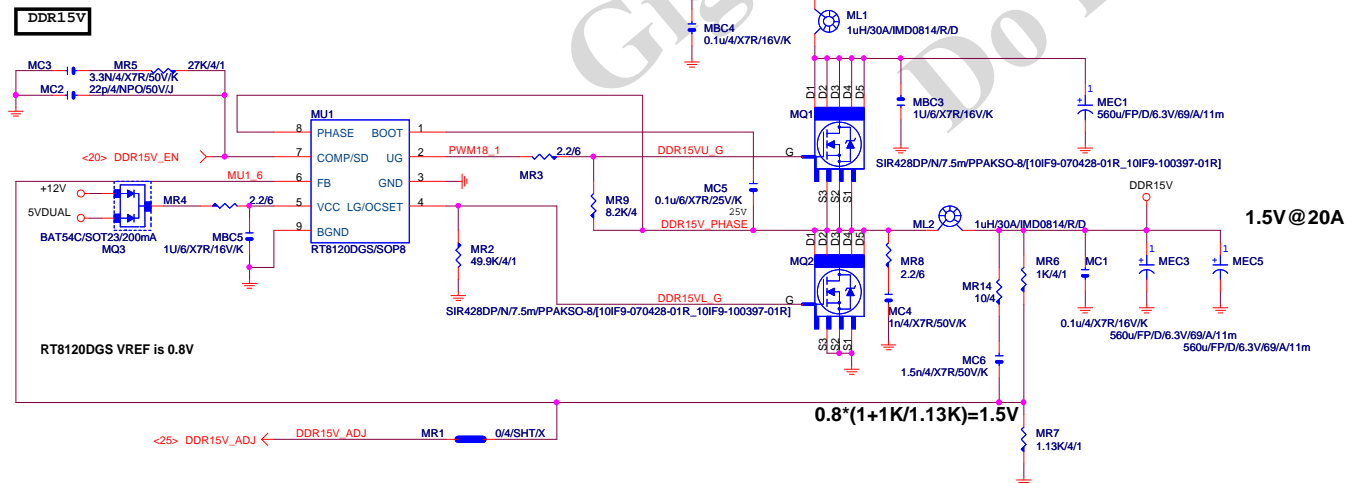
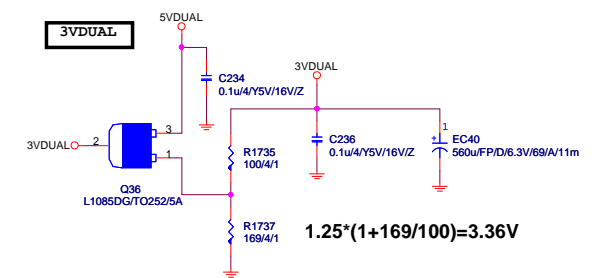
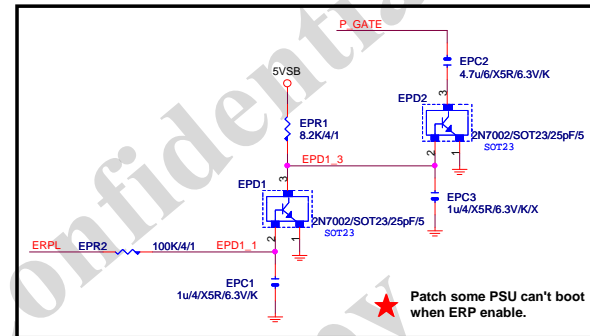
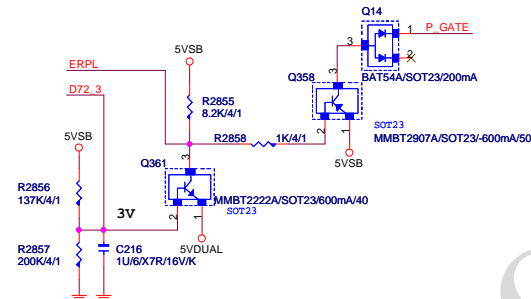
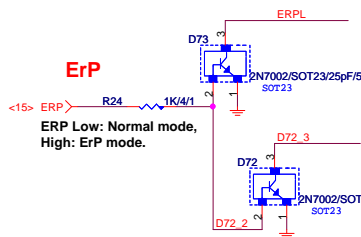
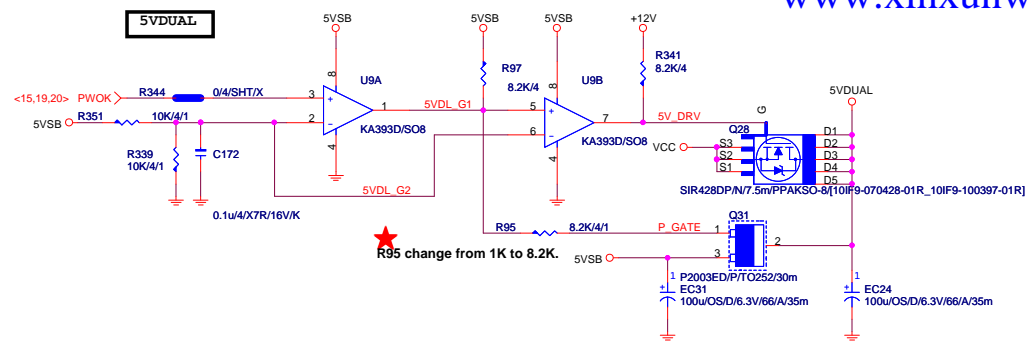
FPC199
0.01u/4/X7R/25V/K
EMI request.



**GIGABYTE™****POWER SEQUENCE**

Title	Document Number	Rev
Customer	GA-F2A85XM-HD3	1.2
Date: Thursday, April 11, 2013	Sheet 20 of 25	





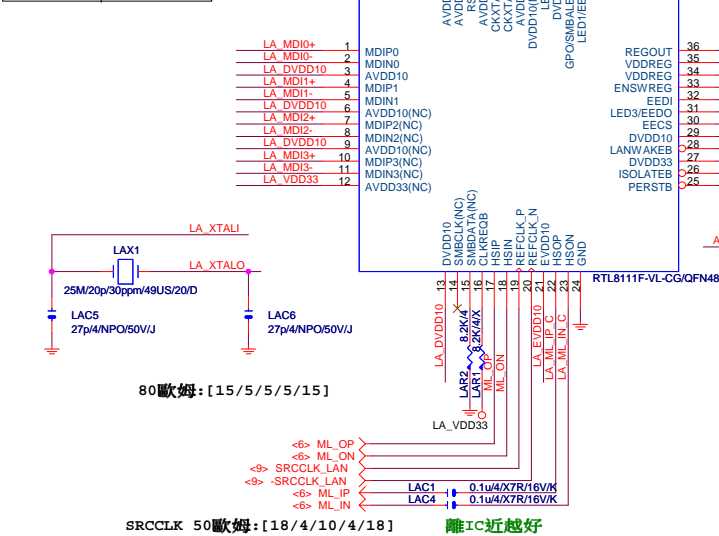
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DDR POWER , 5VDUAL , ErP		
Title	Document Number	Rev
Size	GA-F2A85XM-HD3	1.2
Date	Thursday, April 11, 2013	Sheet 23 of 25

PCIE-1G LAN

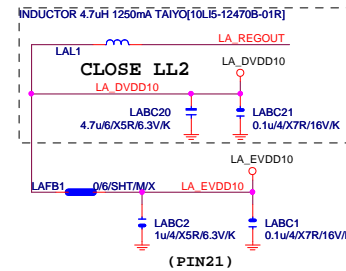
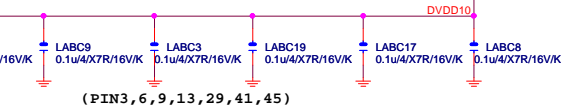
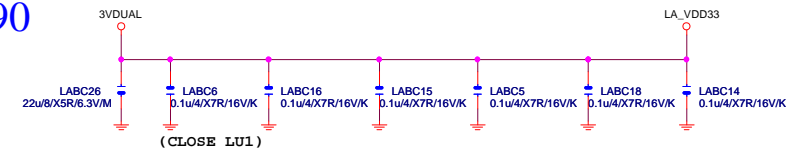
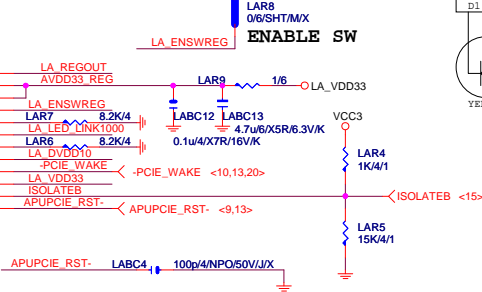
Power domain chart

	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V

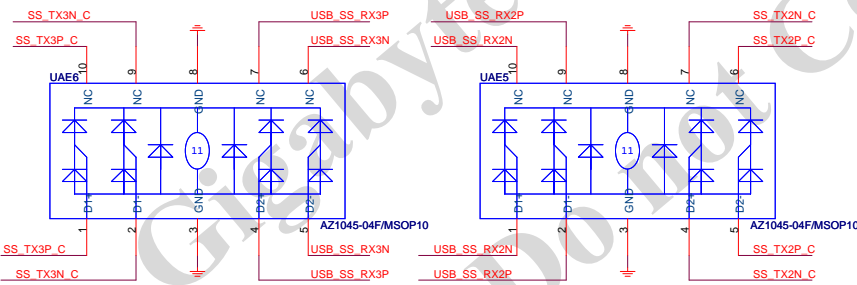
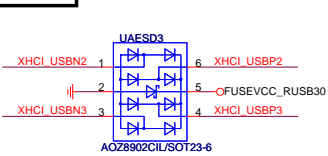


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FOR DSM MODE (DEEP SLEEP MODE)

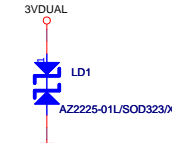
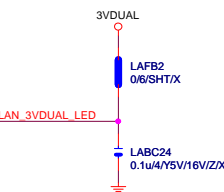
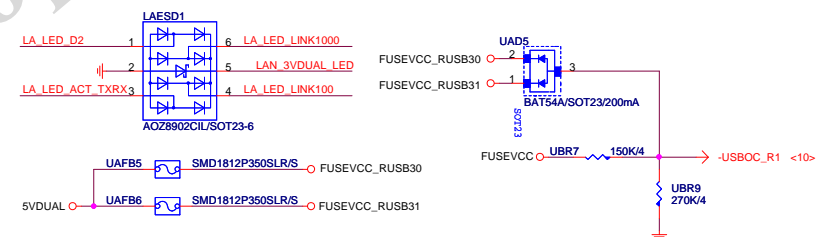
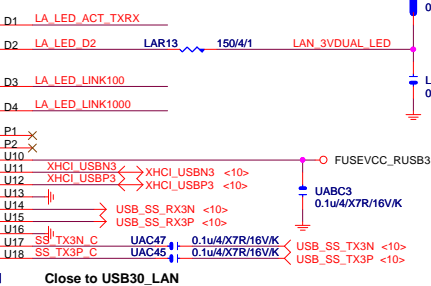
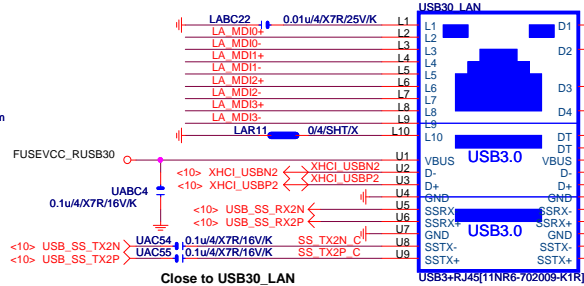


USB_LAN



USB_LAN CONNECTOR

LAN 100 Ohm
USB 90 Ohm



GIGABYTE™

Title	REALTK RTL8111E-VL	Rev	1.2
Size	Document Number	GA-F2A85XM-HD3	
Customer			
Date	Thursday, April 11, 2013	Sheet	24 of 25

